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F – 2103

Reg. No. :

Name :

First Semester B.Sc./B.C.A. Degree Examination, November 2018

Career Related FDP Under CBCSS

Group 2(b) : Computer Science/Computer Applications

Group 2(a) : Physics with Computer Applications

Foundation/Vocational Course

CS 1121/CP 1121/PC 1171

COMPUTER FUNDAMENTALS AND ORGANIZATION

(2018 Admission)

Time : 3 Hours

Max. Marks : 80

SECTION – A

Very short answer type. **One word** to maximum **one sentence**, answer **all** questions. **(10×1=10 Marks)**

1. BIOS is the short name of _____
2. Define input device.
3. Define SRAM.
4. What is an optical disk ?
5. Define Miss in Cache.
6. What is pipelining ?
7. Expand LEA instruction.
8. Define virtual memory.
9. What is a strobe ?
10. Define parallel data transfer.

P.T.O.



SECTION – B

Short answer. **Not** to exceed **one** paragraph, answer **any eight** questions.

Each question carries **two** marks.

(8×2=16 Marks)

11. Write a note on Ribbon Cable.
12. What is the role of an SMPS ?
13. Write about CMOS.
14. Explain about Magnetic Tape.
15. Write about USB.
16. Write a note on Accumulator Register.
17. Write a note on instruction set.
18. Explain about SHL instruction.
19. Write a note on Micro Instruction.
20. Explain about serial communication.
21. Write about daisy chain.
22. Write a note on bus request in DMA.

SECTION – C

Short essay. **Not** to exceed **120** words, answer **any six** questions. **Each** question carries **four** marks.

(6×4=24 Marks)

23. Write a note on expansion cards.
24. Draw Von Neumann architecture.
25. Differentiate SRAM and DRAM.



26. Write a note on type of optical disks.
27. Explain PC and DR registers.
28. Write a note on advantages of RISC architecture.
29. Differentiate CD-R and CD-W.
30. Write about IOP.
31. Write about synchronous data transfer.

SECTION – D

Long essay. Answer **any two** questions. **Each** question carries **15** marks.

(2×15=30 Marks)

32. Explain about components inside a computer in detail.
 33. Write a detailed note on Memory Hierarchy.
 34. Differentiate Interrupt and Instruction Cycle.
 35. Write about Modes of Data Transfer in detail.
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